



COPY OF PAPERS
ORIGINALLY FILED

Attorney Docket No.: 11411.002001

2827

#5/Election/1a
Shawn
5-13-02

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yoshiyuki UCHINONO et al. Art Unit : 2827
Serial No.: 09/995,085 Examiner : Dinh, Tuan T.
Filed : November 27, 2001
Title : MULTILAYER CIRCUIT BOARD AND METHOD OF MANUFACTURING
THE SAME

Assistant Commissioner for Patents
Washington, DC 20231

REPLY UNDER 37 CFR § 1.111

Dear Sir:

In response to the Restriction Requirement dated March 27, 2002, Applicant elects Group I, which includes claims 1-7 and relates to a multiplayer circuit board, for continued prosecution without traverse. Please cancel non-elected claims 8-15.

Please apply any charges not covered, or any credits, to Deposit Account 50-0591 (Reference Number 11411.002001).

Respectfully submitted,

Date: 4/24/02

Jonathan P. Osha, Reg. No. 33,986
Rosenthal & Osha L.L.P.
One Houston Center, Suite 2800
1221 McKinney Street
Houston, TX 77010

Telephone: (713) 228-8600
Facsimile: (713) 228-8778

28665_1

RECEIVED
MAY - 8 2002
TECHNOLOGY CENTER 2800

15

signal, said first converting element having a high voltage to current gain; and
a second converting element having an input for receiving said input voltage signal, and an output coupled to the output of said voltage to current converter for providing a second output current in response to said input voltage signal, said second converting element having a substantially flat fre-

16

quency response in a selected frequency range and a low voltage to current gain relative to the voltage to current gain of said first converting element; wherein the output of said voltage to current converter combines said first and second output currents to derive said control current.

* * * * *